

XILINX INC  
Form 8-K  
March 08, 2005

**UNITED STATES  
SECURITIES AND EXCHANGE COMMISSION**  
Washington, D.C. 20549

**FORM 8-K**

**CURRENT REPORT**  
**Pursuant to Section 13 or 15(d) of the Securities Exchange Act of 1934**

Date of Report (date of earliest event reported): **March 8, 2005**

**XILINX, INC.**  
(Exact name of registrant as specified in its charter)

<b>Delaware</b> (State or other jurisdiction of incorporation)	<b>0-18548</b> (Commission File Number)	<b>77-0188631</b> (IRS Employer Identification No.)
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<b>2100 Logic Drive, San Jose, California</b> (Address of principal executive offices)	<b>95124</b> (Zip Code)
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Registrant's telephone number, including area code: **(408) 559-7778**

Check the appropriate box below if the Form 8-K filing is intended to simultaneously satisfy the filing obligation of the registrant under any of the following provisions:

- Written communications pursuant to Rule 425 under the Securities Act (17 CFR 230.425)

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- o Soliciting material pursuant to Rule 14a-12 under the Exchange Act (17 CFR 240.14a-12)
  
  - o Pre-commencement communications pursuant to Rule 14d-2(b) under the Exchange Act (17 CFR 240.14d-2(b))
  
  - o Pre-commencement communications pursuant to Rule 13e-4(c) under the Exchange Act (17 CFR 240.13e-4(c))
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**Item 7.01 Regulation FD Disclosure**

On March 8, 2005, Xilinx, Inc. issued a press release announcing a business update for the fourth fiscal quarter. A copy of this press release is furnished as Exhibit 99.1 to this report.

**Item 9.01**

**Financial Statements and Exhibits:**

**(c) Exhibits**

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**Exhibit No.**

**Description**

99.1 Press Release of Xilinx, Inc. dated March 8, 2005.

**SIGNATURES**

Pursuant to the requirements of the Securities Exchange Act of 1934, the Registrant has duly caused this report to be signed on its behalf by the undersigned hereunto duly authorized.

XILINX, INC.

Date: March 8, 2005

By: /s/ Thomas R. Lavelle

Thomas R. Lavelle

Vice President and General Counsel

**EXHIBIT INDEX**

<b><u>Exhibit No.</u></b>	<b><u>Description</u></b>
99.1	Press Release of Xilinx, Inc. dated March 8, 2005.